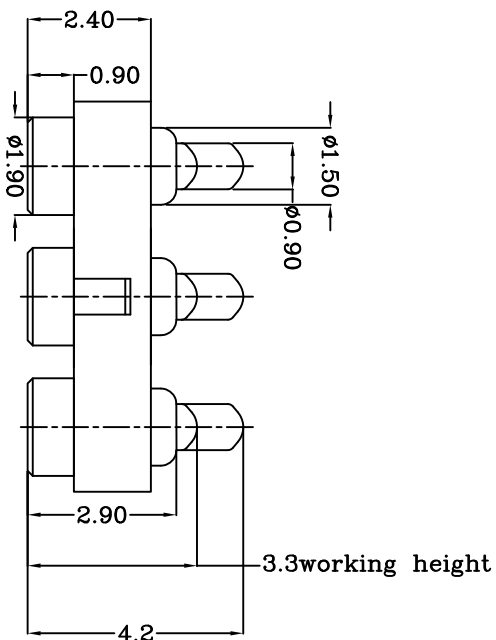
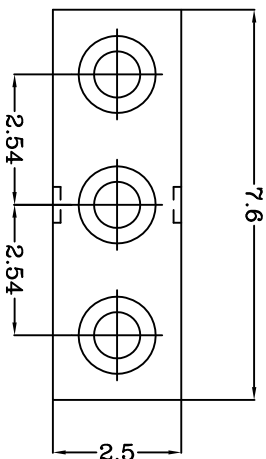
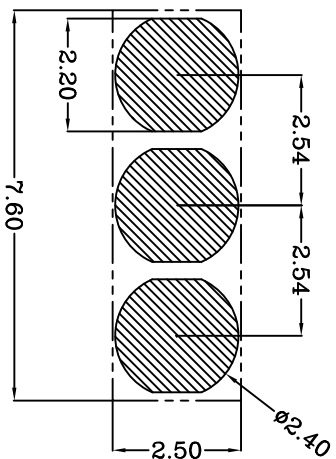


DWG NO.
C-110341



TOP-LINK



Recommended PCB Layout

Plating Plunger: 0.4 μ m Au over 1.4 μ m Ni
Plating Body: 0.1 μ m Au over 1.4 μ m Ni.

	REV		8/08/06/05
LTR	REVISION RECORD	APVD	DATE

DIMENSIONS IN MM TOLERANCES UNLESS OTHER SPECIFIED				MATL 		HT TR —		FIN — 	
0	= ±			APVD	<i>Top-Link Electronics</i> Top-Link Electronics Ltd.				
0.0	= ±0.15			Qln					
0.00	= ±0.10			CHK					
0.000	= ±			Jlm	REL				
ANGLE: ±1°									
SURF TEXTURE: 32/ 				NAME BATTERY PGGD CONNECTOR 3 POSITIONS PGC-1.5-4.2-R-3P-2.54PH					
REMOVE BURRS, BREAK SHARP EDGES, R0.05 MAX DO NOT SCALE PRINT				SCALE	A4	DWG NO.	C-110341	SHEET 1 OF 1	REV A

110341-3
PART NUMBER

USED ON

1